

FIG. 4

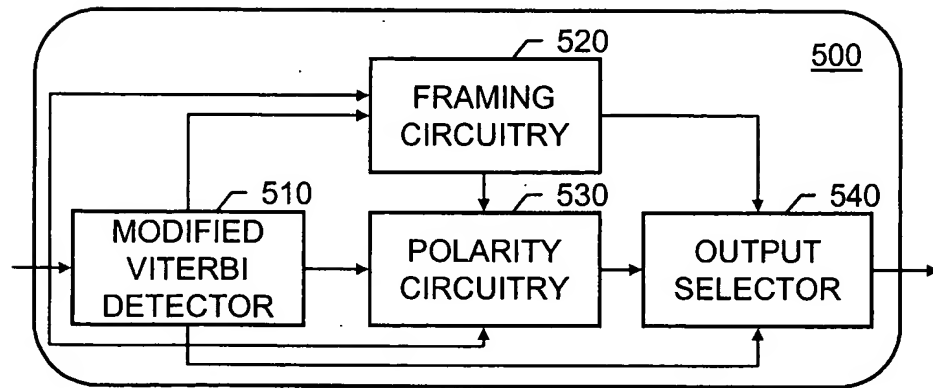


FIG. 5

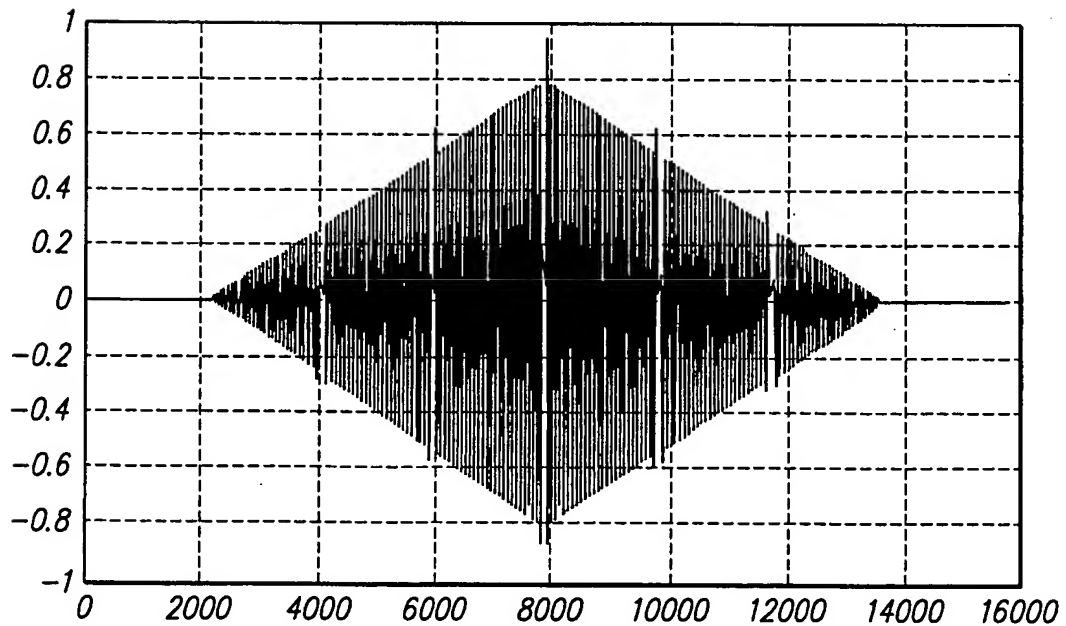


FIG. 6

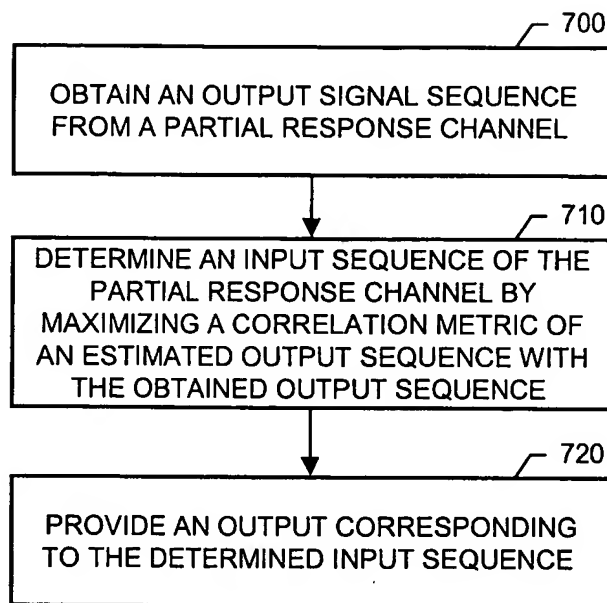


FIG. 7

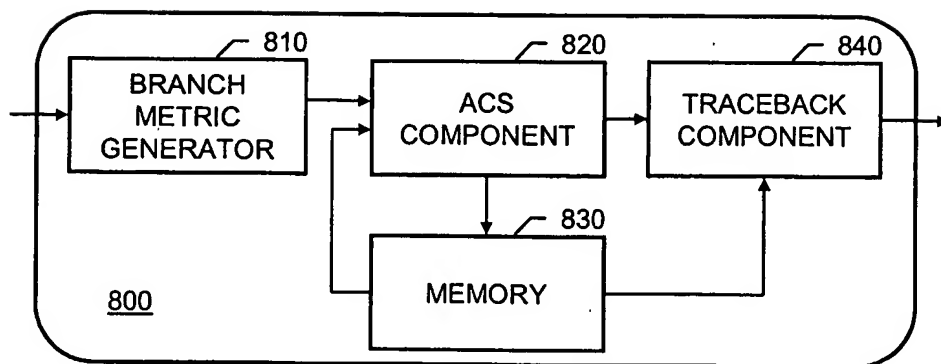


FIG. 8

900 **FIG. 9**

Previous Symbol	Current Symbol	State Transitions	Output Peaks	Outputs for Each Input Symbol	
				Peak sampling	Shoulder sampling
"0"(1100)	"0"(1100)	1100->1100	X0-0 +0-0	1,0,-1,0	1,1,-1,-1
"0"(1100)	"1"(0011)	1100->0011	X0-0 00+0	0,0,1,0	0,0,1,1
"1"(0011)	"0"(1100)	0011->1100	X0+0 00-0	0,0,-1,0	0,0,-1,-1
"1"(0011)	"1"(0011)	0011->0011	X0+0 -0+0	-1,0,1,0	-1,-1,1,1

FIG. 10

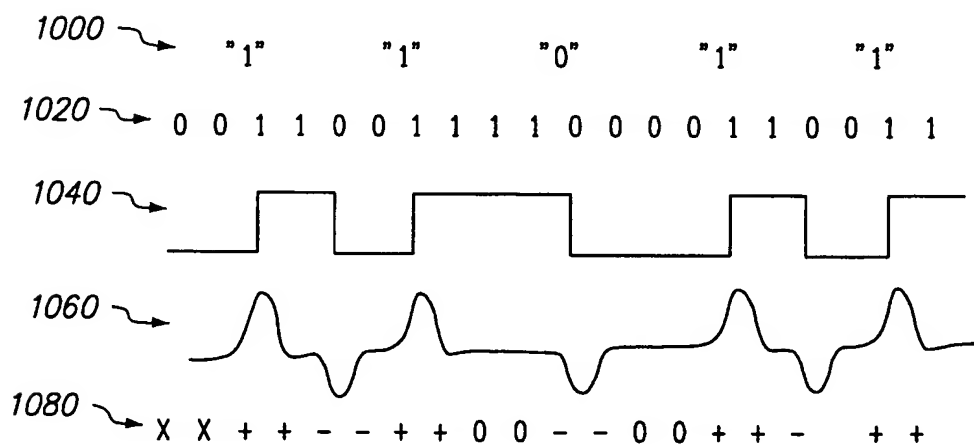
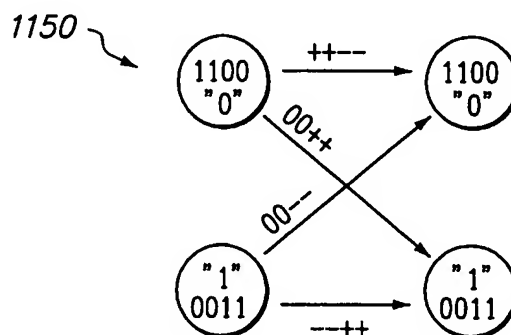


FIG. 11



1200 ↘

FIG. 12

Phase 0		Phase 1		Phase 2		Phase 3	
output	accumulator	output	accumulator	output	accumulator	output	accumulator
0	672	1	1036	1	2074	1	1215
0	672	1	1036	1	2074	0	1215
0	672	0	1036	1	2074	1	1215
0	672	1	1036	1	2074	0	1215
0	672	1	1036	1	2074	0	1216
0	672	1	1036	1	2075	0	1216
1	672	1	1037	1	2076	1	1216
1	673	0	1037	0	2076	0	1216
0	673	1	1037	1	2076	0	1216
0	673	1	1037	1	2076	0	1216
0	673	1	1037	1	2076	0	1216
0	673	1	1037	1	2076	0	1216
0	673	1	1037	1	2076	0	1216
0	673	1	1037	1	2076	1	1216
0	673	0	1037	1	2077	1	1217
0	673	0	1037	1	2078	1	1217
0	673	0	1037	0	2078	0	1218
0	673	1	1037	1	2078	1	1218
0	673	1	1037	1	2078	0	1218
0	673	1	1037	1	2078	0	1218
0	673	1	1037	1	2078	1	1218
0	673	0	1037	1	2078	1	1218
1	674	0	1037	1	2079	1	1218
1	674	0	1038	1	2080	1	1218
0	675	0	1038	0	2080	0	1218
0	675	1	1038	1	2080	0	1218
0	675	0	1038	1	2080	1	1218
0	675	0	1038	1	2080	0	1218
0	675	1	1038	1	2080	0	1218
0	675	1	1038	1	2080	0	1219
1	675	1	1039	1	2081	0	1219
1	675	1	1039	1	2082	1	1219
0	676	0	1039	0	2082	0	1219
0	676	1	1039	1	2082	0	1219
0	676	1	1039	1	2082	0	1219
0	676	1	1039	1	2082	1	1219
0	676	1	1039	1	2082	0	1219
0	676	1	1039	1	2082	1	1219
0	676	0	1039	1	2083	1	1219
0	676	0	1040	1	2085	1	1219

1300 ↘

FIG. 13

Phase 0		Phase 1		Phase 2		Phase 3	
output	accumulator	output	accumulator	output	accumulator	output	accumulator
1	672	0	1036	0	2074	0	1215
1	672	0	1036	0	2074	1	1215
1	672	1	1036	0	2074	0	1251
1	672	0	1036	0	2074	1	1251
1	672	0	1036	0	2074	1	1216
1	672	0	1036	0	2075	1	1216
1	672	0	1037	0	2076	0	1216
0	673	1	1037	1	2076	1	1216
0	673	0	1037	0	2076	1	1216
1	673	0	1037	0	2076	1	1216
1	673	0	1037	0	2076	1	1216
1	673	0	1037	0	2076	1	1216
1	673	0	1037	0	2076	0	1216
1	673	1	1037	0	2077	0	1217
1	673	1	1037	0	2078	0	1217
1	673	1	1037	1	2078	1	1218
1	673	0	1037	0	2078	0	1218
1	673	0	1037	0	2078	1	1218
1	673	0	1037	0	2078	1	1218
1	673	0	1037	0	2078	0	1218
1	673	1	1037	0	2078	0	1218
1	674	1	1037	0	2079	0	1218
1	674	1	1038	0	2080	0	1218
0	675	1	1038	1	2080	1	1218
0	675	0	1038	0	2080	1	1218
1	675	1	1038	0	2080	0	1218
1	675	1	1038	0	2080	1	1218
1	675	0	1038	0	2080	1	1218
1	675	0	1038	0	2080	1	1219
1	675	0	1039	0	2081	1	1219
1	675	0	1039	0	2082	0	1219
0	676	1	1039	1	2082	1	1219
0	676	0	1039	0	2082	1	1219
1	676	0	1039	0	2082	1	1219
1	676	0	1039	0	2082	0	1219
1	676	0	1039	0	2082	1	1219
1	676	0	1039	0	2082	0	1219
1	676	1	1039	0	2083	0	1219
1	676	1	1040	0	2085	0	1219
1	676	1	1040	1	2085	1	1219

[illegible]

[illegible]

	64 sampling phases that cover a complete symbol duration with 16x over-sampling
1	
2	
3	
4	x
5	
6	
7	
8	x
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31	
32	
33	
34	
35	
36	
37	
38	
39	xxxx
40	
41	
42	
43	

[illegible]